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1. An SOS MOSFET model based on calculation of the surface potential
Howes, R.; Redman-White, W.; Nichols, K.G.; Mole, P.J.; Robinson, M.J.; Bird, S.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
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[AbstractPlus](#) | Full Text: [PDF](#)(1184 KB) IEEE JNL



2. A semianalytical parameter extraction of a SPICE BSIM3v3 for RF MOSFET's using S-param
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Microwave Theory and Techniques, IEEE Transactions on
Volume 48, Issue 3, March 2000 Page(s):412 - 416
Digital Object Identifier 10.1109/22.826840
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(140 KB) IEEE JNL



3. Piece-wise linear approximation of MOS nonlinear junction capacitance in high-frequency ci
design
Ma, S.W.; Hei Wong; Ho, C.K.;
Electron Devices and Solid-State Circuits, 2003 IEEE Conference on
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Digital Object Identifier 10.1109/LPE.1995.482474
[AbstractPlus](#) | Full Text: [PDF](#)(192 KB) IEEE CNF



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Radio and Wireless Conference, 2000. RAWCON 2000. 2000 IEEE
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Digital Object Identifier 10.1109/RAWCON.2000.881890

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Secker, D.A.; Prince, J.L.;

Components, Packaging, and Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions on
Components, Hybrids, and Manufacturing Technology, IEEE Transactions on
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Enz, C.;

Microwave Theory and Techniques, IEEE Transactions on
Volume 50, Issue 1, Part 2, Jan. 2002 Page(s):342 - 359

Digital Object Identifier 10.1109/22.981286

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10. History-effect-conscious SPICE model extraction for PD-SOI technology

Goo, J.-S.; An, J.X.; Thuruthiyil, C.; Ly, T.; Chen, Q.; Radwin, M.; Zhi-Yuan Wu; Lee, M.S.L.; Zamu
Assad, F.; Pelella, M.M.; Icel, A.B.;

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4-7 Oct. 2004 Page(s):156 - 158

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[AbstractPlus](#) | Full Text: [PDF\(224 KB\)](#) IEEE CNF



12. Mixed-signal performance of sub-100nm fully-depleted SOI devices with metal gate, high K (and elevated source/drain extensions

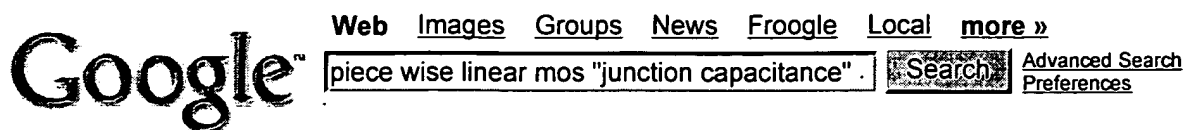
Vandooren, A.; Thean, A.V.Y.; Du, Y.; To, I.; Hughes, J.; Stephens, T.; Huang, M.; Egley, S.; Zaval
Barr, A.; White, T.; Samavedam, S.; Mathew, L.; Schaeffer, J.; Triyoso, D.; Rossow, M.; Roan, D.; I
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Digital Object Identifier 10.1109/IEDM.2003.1269441

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The **MOS** gate capacitances, as a nonlinear function of the terminal voltages, are modeled by JE Meyer's **piece-wise linear** model [13]. ...

qucs.sourceforge.net/tech/node42.html - 78k - [Cached](#) - [Similar pages](#)

Circuit Elements

These two functions are useful in sythesizing **piece-wise non-linear** functions, ... A discontinuity in the **MOS** level 3 model with respect to the KAPPA ...

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EEDesign.com - ACAD fields hybrid power analyzer

... can input their current waveform in **piece-wise linear** format," he said ... to get more accurate **junction capacitance** information for **MOS** gates, because ...

www.eedesign.com/news/showArticle.jhtml?articleId=17408076&kc=4217 - 56k - Supplemental Result - [Cached](#) - [Similar pages](#)

SPICE Instructions

The resultant **linear** circuit is then analyzed over a user-specified range of ... exponential, sinusoidal, **piece-wise linear**, and single-frequency FM. ...

www.doe.carleton.ca/~cp/radio/spice.html - 39k - [Cached](#) - [Similar pages](#)

SPICE Instructions

Piece-Wise Linear PWL(T1 V1). Examples: ... DEFL=x resets the value for **MOS** channel length; the default is 100.0 micrometer. DEFW=x resets the value for ...

www.doe.carleton.ca/~cp/radio/spicelong.html - 96k - [Cached](#) - [Similar pages](#)

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... 1-exp(-(time-TD2)/TAU2)) 4. **Piece-Wise Linear** PWL(T1 ... The default is the piecewise **linear** voltage-dependent ... S. Liu, "The Simulation of **MOS** Integrated Circuits ...

pdp-10.trailing-edge.com/decuslib20-03/01/decus/20-0079/spice.guide.html - 101k - Supplemental Result - [Cached](#) - [Similar pages](#)

[PDF] CIRCUIT ELEMENTS AND MODELS

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... functions are useful in sythesizing **piece-wise non-linear** ... and if so uses **linear** interpolation; otherwise it ... with a zero-bias **junction capacitance** equivalent to ...

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are useful in sythesizing **piece-wise non-linear** functions, though convergence may be ... bias **junction capacitance** equivalent to the capacitance replaced, ...

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[PDF] stdin (ditroff)

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functions: pulse, exponential, sinusoidal, **piece-wise linear**, ... resets the value for **MOS** channel length; the default is 100.0 micrometer. ...

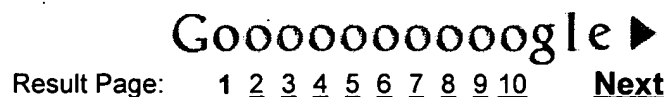
min.ecn.purdue.edu/~bashir/ee455_f01/tutorial.pdf - Similar pages

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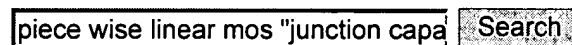
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These two functions are useful in synthesizing **piece-wise non-linear** functions, ... mjsw Source drain side **junction capacitance** grading coefficient cj Source ...
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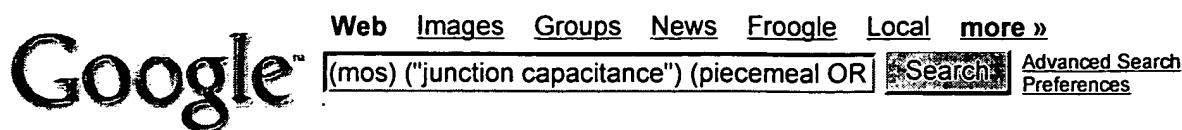
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SPICE3 MOS Models

WRspice provides the basic MOS models provided in SPICE2/3, ... Charge storage effects are modeled by the **piecewise** linear voltage dependent capacitance ...

www.wrcad.com/manual/wrsmanual/node119.html - 31k - [Cached](#) - [Similar pages](#)

Josephson Junction Model

cap, *, **Junction capacitance**, F, 1.0e-12, 5.0e-13 ... Rtype=4 uses a **piecewise**-linear quasiparticle characteristic identical to rtype=1, however the gap ...

www.wrcad.com/manual/wrsmanual/node120.html - 11k - [Cached](#) - [Similar pages](#)

Department of Electrical and Electronic Engineering

... ideal diode, diode resistance, **piecewise** linear model ... the ideal, Reverse-bias **junction capacitance**, Schottky diodes ... semiconductor transistors The MOS capacitor. ...

uofk.edu/faculties/engineering/electrical/contents/Electronic-computer/EC2208.htm - 23k - Supplemental Result - [Cached](#) - [Similar pages](#)

SPICE Version 2G User's Guide: Semiconductor Devices

... The default is the **piecewise** linear voltage-dependent ... F 0.0 20FF 10 CBS zero-bias BS **junction capacitance**. ... S. Liu, "The Simulation of MOS Integrated Circuits ...

bear.ces.cwru.edu/eecs_cad/man_spice2_sec3.html - 21k - Supplemental Result - [Cached](#) - [Similar pages](#)

MOSFET Models (NMOS/PMOS)

... Charge storage effects are modeled by the **piecewise** linear voltages ... A discontinuity in the MOS level 3 model with respect to ... zero-bias BD **junction capacitance**. F. ...

www.autotraxeda.com/.../Circuit_Elements_and_Models/Transistors_and_Diodes/MOSFET_Models_NMOS_PMOS.htm - 60k - Supplemental Result -

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OUP: Sedra/Smith and Dimitrijevic Package

... 3.3.5. The **Piecewise**-Linear Model; 3.3.6. The Constant ... 4.6.9. Summary; 4.7 Single-Stage MOS Amplifiers; 4.7.1 ... 5.8.2. The Base-Emitter **Junction Capacitance** Cje; ...

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Oxford University Press

The **Piecewise**-Linear Model 3.3.6.. The Constant-Voltage-Drop Model 3.3.7..

The Ideal-Diode Model ... Combining MOS and Bipolar Transistors--BiCMOS Circuits ...

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SPICE 3 User's Manual - Appendix B

mj, Source drain bottom **junction capacitance** grading coefficient ... Mos1 - model parameters (output-only). type, N-channel or P-channel MOS ...

newton.ex.ac.uk/teaching/CDHW/Electronics2/userguide/secB.html - 94k - [Cached](#) - [Similar pages](#)

Circuit Elements

Charge storage effects are modeled by the **piecewise** linear ... A discontinuity in the MOS level 3 model with respect to the KAPPA parameter has been ...

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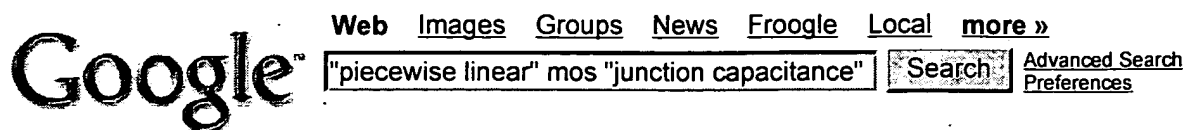
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(mos) ("junction capacitance") (piece

<http://www.google.com/search?hl=en&lr=&rls=GGLD%2CGGLD%3A2004-30%2CGGLD...> 8/31/05



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3.4.8. MOSFET Models (NMOS/PMOS)

... storage effects are modeled by the **piecewise linear** voltages-dependent ... A discontinuity in the **MOS** level 3 model ... 9 CBD zero-bias BD **junction capacitance** F 0.0 ...
www1.u-netsurf.ne.jp/~future/HTML/spice3f5_html/3.4.8.html - 16k - Supplemental Result -
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side **junction capacitance** grading coefficient cj Source drain bottom ...
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[PDF] SCPES CMOS C

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... consists of the inverter drain **junction capacitance**, the gate ... Current Modeling Choices
(a) **Piecewise linear** (b) Piecewise ... the simplified bulk-charge **MOS** model[8 ...
sigda.org/Archives/ProceedingArchives/ Compendiums/papers/2001/aspdac01/pdf/files/7b_4.pdf - Supplemental
Result - [Similar pages](#)

Level 27 SOSFET Model

... is available to all **MOS** DC equations ... resistance calculation estimates the **junction capacitance**, saturation current ... allow for a **piecewise linear** approximation to ...
siloom.han.ac.kr/~young/ data/hspice_html/hspice-152.html - 33k - Supplemental Result - [Cached](#) - [Similar pages](#)

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A simpler approach is to use a **piecewise linear** approximation for the VTC, as
... capacitor to the value of the **junction capacitance** under zero-bias ...
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bias **junction capacitance** equivalent to the capacitance replaced, ... modeled by
the **piecewise linear** voltages-dependent capacitance model proposed by Meyer ...
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[PDF] A study of phase noise in CMOS oscillators

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the differential pair (ie, the source **junction capacitance** of ... introduce it
into the circuit as a SPICE **piecewise linear** ...
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[PDF] B. APPENDIX B: MODEL AND DEVICE PARAMETERS

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description. sffm. single freq. FM description ...
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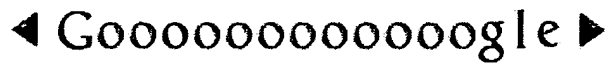
junction capacitance. equations 16-37. model parameters 16-11. junction capacitor
16-6 ... **piecewise linear** source. See demo files, PWL. pll 31-27 ...
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consists of the drain **junction capacitance** of INV2, ... the **piecewise-linear** of V.
mid. is employed. The solving process of. (2) is similar to that in Sec. ...

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 Ma, S.W.; Hei Wong; Ho, C.K.;
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 16-18 Dec. 2003 Page(s):233 - 236
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 1969 International Electron Devices Meeting
 Volume 15, 1969 Page(s):76 - 76
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 Volume 25, Issue 6, Jun 1978 Page(s):628 - 639
[AbstractPlus](#) | Full Text: [PDF](#)(1144 KB) IEEE JNL
6. Digitally-tunable continuous-time filters with high signal linearity
 Durham, A.M.; Redman-White, W.; Hughes, J.B.;
 Digital and Analogue Filter and Filtering Systems, IEE Colloquium on

13 Dec 1991 Page(s):9/1 - 9/4

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1. Design of Class E power amplifier with nonlinear parasitic output capacitance

Alinikula, P.; Choi, K.; Long, S.I.;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circ Express Briefs, IEEE Transactions on]

Volume 46, Issue 2, Feb. 1999 Page(s):114 - 119

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2. Piece-wise linear approximation of MOS nonlinear junction capacitance in high-frequency circuit design

Ma, S.W.; Hei Wong; Ho, C.K.;

Electron Devices and Solid-State Circuits, 2003 IEEE Conference on 16-18 Dec. 2003 Page(s):233 - 236

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3. Accurate timing simulation of gallium arsenide circuits

Bernhardt, B.A.; Sobelman, G.E.;

Circuits and Systems, 1991., IEEE International Symposium on 11-14 June 1991 Page(s):2407 - 2410 vol.4

Digital Object Identifier 10.1109/ISCAS.1991.176864

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Durham, A.M.; Redman-White, W.; Hughes, J.B.;

Digital and Analogue Filter and Filtering Systems, IEE Colloquium on 13 Dec 1991 Page(s):9/1 - 9/4

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5. Femtojoule high speed planar GaAs E-JFET logic

Zuleeg, R.; Nothoff, J.K.; Lehovc, K.;

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Ma, S.W.; Hei Wong; Ho, C.K.;

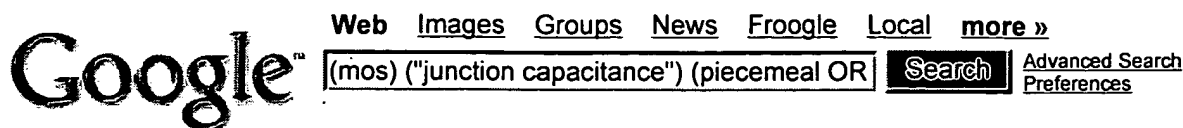
Electron Devices and Solid-State Circuits, 2003 IEEE Conference on

16-18 Dec. 2003 Page(s):233 - 236

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SPICE3 MOS Models

WRspice provides the basic MOS models provided in SPICE2/3, ... Charge storage effects are modeled by the **piecewise** linear voltage dependent capacitance ...

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Josephson Junction Model

cap, *, **Junction capacitance**, F, 1.0e-12, 5.0e-13 ... Rtype=4 uses a **piecewise**-linear quasiparticle characteristic identical to rtype=1, however the gap ...

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Department of Electrical and Electronic Engineering

... ideal diode, diode resistance, **piecewise** linear model ... the ideal, Reverse-bias **junction capacitance**, Schottky diodes ... semiconductor transistors The MOS capacitor. ...

uofk.edu/faculties/engineering/electrical/contents/Electronic-computer/EC2208.htm - 23k - Supplemental Result - [Cached](#) - [Similar pages](#)

SPICE Version 2G User's Guide: Semiconductor Devices

... The default is the **piecewise** linear voltage-dependent ... F 0.0 20FF 10 CBS zero-bias BS **junction capacitance**. ... S. Liu, "The Simulation of MOS Integrated Circuits ...

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MOSFET Models (NMOS/PMOS)

... Charge storage effects are modeled by the **piecewise** linear voltages ... A discontinuity in the MOS level 3 model with respect to ... zero-bias BD **junction capacitance**. F. ...

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OUP: Sedra/Smith and Dimitrijevic Package

... 3.3.5. The **Piecewise**-Linear Model; 3.3.6. The Constant ... 4.6.9. Summary; 4.7 Single-Stage MOS Amplifiers; 4.7.1 ... 5.8.2. The Base-Emitter **Junction Capacitance** Cje; ...

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Oxford University Press

The **Piecewise**-Linear Model 3.3.6.. The Constant-Voltage-Drop Model 3.3.7..

The Ideal-Diode Model ... Combining MOS and Bipolar Transistors--BiCMOS Circuits ...

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SPICE 3 User's Manual - Appendix B

mj, Source drain bottom **junction capacitance** grading coefficient ... Mos1 - model parameters (output-only). type, N-channel or P-channel MOS ...

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Circuit Elements

Charge storage effects are modeled by the **piecewise** linear ... A discontinuity in the MOS level 3 model with respect to the KAPPA parameter has been ...

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piecewise Vf characteristic (Figure 3). The challenge, here is to build a satisfactory RF switch ... **junction capacitance** will now parasitically load the ...

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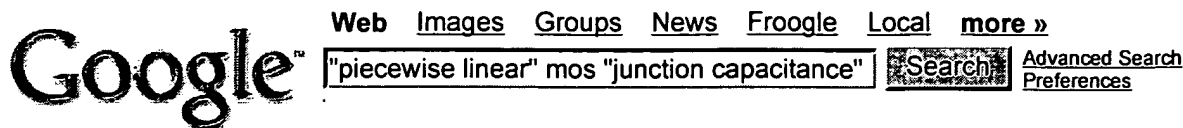
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mj, Source drain bottom **junction capacitance** grading coefficient ... pwl,

Piecewise linear description. sffm, single freq. FM description ...

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cap, *, **Junction capacitance**, F, 1.0e-12, 5.0e-13 ... Rtype=4 uses a **piecewise-linear** quasiparticle characteristic identical to rtype=1, however the gap ...

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Table of contents

Introduction to Electronics; Operational Amplifiers; Diodes; The **MOS** Field-Effect

Transistor ... Emitter **Junction Capacitance** Cje; 5.8.3 The Collector? ...

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EE 603 Power Electronics

You will need to use the **piecewise linear** source to create the triangle ...

c) How do you compute the diode's **junction capacitance** from the measured current ...

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You will need to use the **piecewise linear** source to create the triangle waveform.

... How do you compute the diode's **junction capacitance** from the measured ...

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discontinuity in the **MOS** level 3 model with ... zero-bias BD **junction capacitance**. F. ...

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Impact ionization is available to all **MOS** DC equations. ... The parameters VNDS

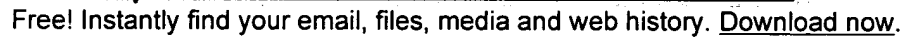
and NDS allow for a **piecewise linear** approximation to the reverse junction ...

www.ece.cmu.edu/~ee762/hspice-docs/ html/hspice_and_qrg/hspice_2001_2-163.html - 32k -

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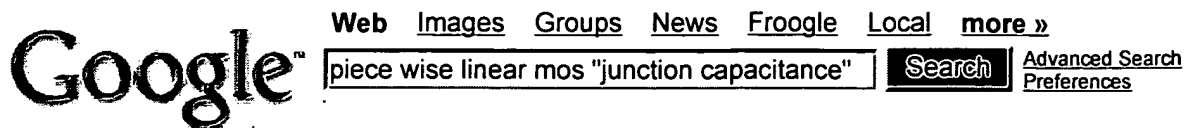
Piecewise linear source (PWL). Vname N1 N2 PWL(T1 V1 T2 V2 T3 V3 ...) ... The MOS transistor name (Mname) has to start with aM; ND, NG, NS and NB are the ...
bach.ece.jhu.edu/~etienne/spice.overview.html - 32k - [Cached](#) - [Similar pages](#)

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EEDesign.com - ACAD fields hybrid power analyzer

... can input their current waveform in **piece-wise linear** format," he said ... to get more accurate **junction capacitance** information for **MOS** gates, because ...

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The resultant **linear** circuit is then analyzed over a user-specified range of ... exponential, sinusoidal, **piece-wise linear**, and single-frequency FM. ...

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SPICE Instructions

Piece-Wise Linear PWL(T1 V1). Examples: ... DEFL=x resets the value for **MOS** channel length; the default is 100.0 micrometer. DEFW=x resets the value for ...

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... 1-exp(-(time-TD2)/TAU2)) 4. **Piece-Wise Linear** PWL(T1 ... The default is the piecewise **linear** voltage-dependent ... S. Liu, "The Simulation of **MOS** Integrated Circuits ...

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... functions are useful in sythesizing **piece-wise** non-linear ... and if so uses **linear** interpolation; otherwise it ... with a zero-bias **junction capacitance** equivalent to ...

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are useful in sythesizing **piece-wise** non-linear functions, though convergence may be ... bias **junction capacitance** equivalent to the capacitance replaced, ...

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functions: pulse, exponential, sinusoidal, **piece-wise linear**, ... resets the value for **MOS** channel length; the default is 100.0 micrometer. ...

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These two functions are useful in synthesizing **piece-wise** non-linear functions,

... mjsw Source drain side **junction capacitance** grading coefficient cj Source ...

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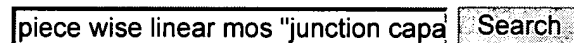
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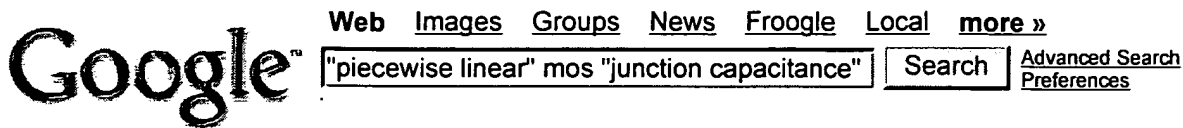
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3.4.8. MOSFET Models (NMOS/PMOS)

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... consists of the inverter drain **junction capacitance**, the gate ... Current Modeling Choices
(a) **Piecewise linear** (b) Piecewise ... the simplified bulk-charge **MOS** model[8 ...
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Level 27 SOSFET Model

... is available to all **MOS** DC equations ... resistance calculation estimates the **junction capacitance**, saturation current ... allow for a **piecewise linear** approximation to ...
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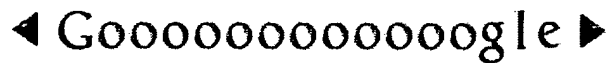
junction capacitance. equations 16-37. model parameters 16-11. junction capacitor
16-6 ... **piecewise linear** source. See demo files, PWL. pli 31-27 ...
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consists of the drain **junction capacitance** of INV2, ... the **piecewise-linear** of V.
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Enz, C.;
Microwave Theory and Techniques, IEEE Transactions on
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